

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re application of:** Rajska et al.

**Application No.** 09/620,021

**Filed:** July 20, 2000

**Confirmation No.** 3823

**For:** CONTINUOUS APPLICATION AND  
DECOMPRESSION OF TEST PATTERNS  
TO A CIRCUIT-UNDER-TEST

**Examiner:** Phung M. Chung

**Art Unit:** 2133

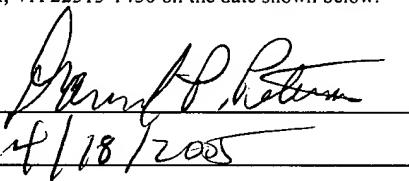
**Attorney Reference No.** 1011-54375-01

**CERTIFICATE OF MAILING**

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for Applicants

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 C.F.R. § 1.97(b)(4)**

Listed on the accompanying form PTO-1449 and enclosed herewith are English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Applicants filed this Information Disclosure Statement ("IDS") before the mailing of a first Office action after the filing of a request for continued examination, and thus believe that no fee is required. However, if the Patent Office determines that a fee is required, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

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|--|--|------------------------|----------------|
| <b>INFORMATION DISCLOSURE STATEMENT<br/>BY APPLICANT</b> |  | Attorney Docket Number | 1011-54375-01  |
|  |  | Application Number     | 09/620,021     |
|  |  | Filing Date            | July 20, 2000  |
|  |  | First Named Inventor   | Rajski         |
|  |  | Art Unit               | 2133           |
|  |  | Examiner Name          | Phung M. Chung |

### U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

| Examiner's Initials* | Cite No. (optional) | Number | Publication Date | Name of Applicant or Patentee |
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### FOREIGN PATENT DOCUMENTS

| Examiner's Initials* | Cite No. (optional) | Country | Number | Publication Date | Name of Applicant or Patentee |
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### OTHER DOCUMENTS

|                      |                     |   |
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| Examiner's Initials* | Cite No. (optional) | Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," <i>IEEE VLSI Test Symp.</i> , pp. 137-142 (1992).                   |
|                      |                     | Zacharia et al., "Two-Dimensional Test Data Decompressor for Multiple Scan Designs," <i>International Test Conference</i> , pp. 186-194 (1996). |
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